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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants:	Chandan Mathur, Scott Hellenbach, John W. Rapp, Larry Jackson, Mark Jones, and Troy Cherasaro
Title:	COMPUTING ARCHITECTURE AND RELATED SYSTEM AND METHOD
Serial Number:	10/684,102
Filing Date:	October 9, 2003
Examiner/Unit:	/ 2185
Attorney Docket No.:	1934-11-3

CERTIFICATE OF MAILING OR TRANSMISSION

I hereby certify that this correspondence is being deposited in the United States Postal Service as First Class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 14th day of March, 2005.

Kelly Pedersen
Signature

RULE 56 INFORMATION DISCLOSURE AND ACCOMPANYING SECTION 1.97(e) CERTIFICATION

TO THE COMMISSIONER FOR PATENTS:

In compliance with 37 CFR § 1.56 and § 1.97, Applicants acting by and through the attorney of record, hereby discloses patents and other documents listed on the attached Form PTO-1449 which may be considered relevant to the examination of the subject application.

Copies of the cited non-U.S. patent references are enclosed.

Certification Under Section 1.97(e)

Each item of information contained in this Information Disclosure Statement was first cited in any communication from a foreign Patent Office in a counterpart foreign application not more than three months prior to the filing of this statement.

Respectfully submitted,

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

Sheet 1 of 2

Complete if Known	
Application Number	10/684,102
Filing Date	October 9, 2003
First Named Inventor	Chandan Mathur
Art Unit	2185
Examiner Name	
Attorney Docket Number	1934-11-3

U.S. PATENT DOCUMENTS

Examiner Initials *	Cite No.[1]	Document Number Number - Kind Code[2] (*if known*)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		US-5,583,964	12/10/96	Wang	
		US-			
		US-			
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FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No.[1]	Foreign Patent Document Country Code[3] - Number[4] - Kind Code[5] (*if known*)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T[6]
		EP 1 061 438 A1	12/20/00	Hewlett-Packard Company		
		EP 0 945 788 A2	09/29/99	Texas Instruments Incorporated		
		EP 0 945 788 A3	06/26/02	Texas Instruments Incorporated		

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This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Sheet 2 of 2

Complete if Known	
Application Number	10/684,102
Filing Date	October 9, 2003
First Named Inventor	Chandan Mathur
Art Unit	2185
Examiner Name	
Attorney Docket Number	1934-11-3

NON PATENT LITERATURE DOCUMENTS

Examiner Initials *	Cite No.[1]	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T [2]
		LECURIEUX-LAFAYETTE G: "Un Seul FPGA Dope Le Traitement D'Images", Electronique, CEP Communication, Paris, FR, no. 55, 1996, pp. 98, 101-103	
		VERMEULEN F. ET AL., "Flexible Hardware Acceleration for Multimedia Oriented Microprocessors", Micro-33. Proceedings of the 33rd Annual ACM/IEEE International Symposium on Microarchitecture. Monterey, CA, Dec. 10-13, 2000, Proceedings of the Annual ACM/IEEE International Symposium on Microarchitecture, Los Alamitos, CA: IEEE Comp. Soc, US, 10 December 2000, pp. 171-177	
		International Search Report for PCT/US 03/3455, December 21, 2004	

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